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*Litigation search**08/447,717*


1 / 1 PLUSPAT - ©QUESTEL-ORBIT - image

**PN** -  US5302966 A 19940412 [US5302966]  
**TI** - (A) Active matrix electroluminescent display and method of operation  
**PA** - (A) SARNOFF DAVID RES CENTER (US)  
**PA0** - David Sarnoff Research Center, Inc., Princeton NJ [US]  
**IN** - (A) STEWART ROGER G (US)  
**AP** - US89246492 19920602 [1992US-0892464]  
**PR** - US89246492 19920602 [1992US-0892464]  
**IC** - (A) G09G-003/30  
**ICAA** - G09G-003/30 [2006-01 A - I R M EP]  
**ICCA** - G09G-003/30 [2006 C - I R M EP]  
**EC** - G09G-003/30  
**ICO** - S09G-201/03  
           S09G-201/06  
           S09G-213/14B  
**PCL** - ORIGINAL (O) : 345076000; CROSS-REFERENCE (X) : 315169300  
           345077000 345691000  
**DT** - Corresponding document  
**CT** - US3590156; US3761617; US4006383; US4087792; US4114070; US4482841;  
           US4528480; US4532506; US4554539; US4602192; US4613793; US4652872;  
           US4736137; US4797667; US4954747; US4958105; US4962374; US4963861;  
           US4975691; US5003302; US5028916; US5079483; US5095248; US5172032  
           P. De Visschere, I. De Rycke, J. Doutreloigne, J. Vanfleteren, "Active Matrix  
           CdSe TFT Addressed Electroluminescent Displays" 1988 IEEE International  
           Research Conf. pp. 74-76.  
  
           J. Vanfleteren, et al. "Evaluation of a 64.times.64 CdSe TFT Addressed  
           ACTFEL Display Demonstrator", 1991 IEEE, pp. 134-136.  
  
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           Digest pp. 334-347.  
  
           J. P. Salerno, et al, "Single-Crystal Silicon Transmissive AMLCD" SID 92  
           Digest pp. 63-66, .COPYRGT.1992.  
  
           J. Vanfleteren, et al "Design of A Prototype Active Matrix CdSe TFT Addressed  
           EL Display" Sep. 24, 1991.  
**STG** - (A) United States patent  
**AB** -  
           An active matrix electroluminescent display (AMELD) having an improved light  
           emitting efficiency and methods of operating the AMELD to produce gray scale  
           operation comprises a plurality of pixels, each pixel including a first transistor  
           having its gate connected to a select line, its source connected to a data line and  
           its drain connected to the gate of a second transistor, the second transistor having  
           its source connected to the data line and its drain connected to a first electrode of

an electroluminescent (EL) cell. The EL cell's second electrode is connected to alternating high voltage means. A method for producing gray scale performance including the step of varying the length of time the second transistor is on while the alternating voltage is applied to the EL cell is also disclosed.


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**PN** -  US5302966 A 19940412 [US5302966]  
**AP** - US89246492 19920602 [1992US-0892464]  
**ACT** - 19920716 US/AS02-A  
ASSIGNMENT OF ASSIGNOR'S INTEREST  
OWNER: DAVID SARNOFF RESEARCH CENTER, INC. A CORP. OF DE;  
EFFECTIVE DATE: 19920714  
  
19920716 US/AS02-A  
ASSIGNMENT OF ASSIGNOR'S INTEREST  
OWNER: STEWART, ROGER G.; EFFECTIVE DATE: 19920714  
  
19950725 US/RF-A  
REISSUE APPLICATION FILED  
EFFECTIVE DATE: 19950523  
**UP** - 2003-22

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**PN** -  5,302,966 A 19940412 [US5302966]  
**PA** - Sarnoff, David Research Center Inc  
**ACT** - 19950523 REISSUE REQUESTED  
Issue Date of O.G.: 19950725  
Reissue Request Number: 08/447717  
Examination Group responsible for Reissue process: 2609

19990301 REASSIGNED  
MERGER

Assignor: DAVID SARNOFF RESEARCH CENTER, INC. DATE SIGNED:  
04/04/1997

Assignee: SARNOFF CORPORATION N201 WASHINGTON ROAD, CN  
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Reel 009773/Frame 0420

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Search statement 2

LEVEL 1 - 1 OF 1 PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

5302966

April 12, 1994

Active matrix electroluminescent display and method of  
operation

REISSUE: May 23, 1995 - Reissue Application filed Ex. Gp.: 2609; Re. S.N.  
08/447,717 (O.G. July 25, 1995)

APPL-NO: 892464 (07)

FILED-DATE: June 2, 1992

GRANTED-DATE: April 12, 1994

CORE TERMS: transistor, pixel, layer, cell, gate, electrode, high voltage,  
voltage, sub, drain ...

**LEXIS-NEXIS**  
**Library: PATENTS**  
**File: ALL**

5,302,966 OR 5302966

**LEXIS-NEXIS**  
**Library: PATENTS**  
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**File: JNLS**

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**File: CURNWS**

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